INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Docket: 1011-54375		App: 09/620,021
	Applicant: Rajski et al.		
	Filed: July 20, 2000		Art Unit: 2133

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Init.*		Number	Date	Name	Class	Sub	Filed
PMC		3,614,400	Oct-71	Farnett			
		3,700,869	Oct-72	Low et al.			
		4,503,537	Mar-85	McAnney			
		4,687,988	Aug-87	Eichelberger et al.			
		4,754,215	Jun-98	Kawai			
		4,785,410	Nov-88	Hamatsu et al.			
		4,801,870	Jan-89	Eichelberger et al.			
		4,860,236	Aug-89	McLeod et al.			
		4,959,832	Sep-90	Bardell, Jr.			
		4,974,184	Nov-98	Avra			
		5,090,035	Feb-92	Murase			
		5,138,619	08/11/92	Fasang et al.			
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		5,268,949	Dec-93	Watanabe			
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		5,325,367	Jun-94	Dekker et al.			
PMC		5,369,648	Nov-94	Nelson			

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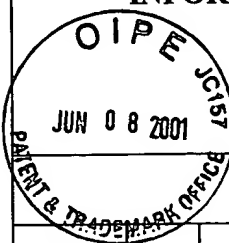
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		5,412,665	May-95	Gruodis et al.			
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		5,446,683	Aug-95	Mullen et al.			
		5,450,414	Sep-95	Lin			
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PC		5,974,433	Oct-99	Currie			
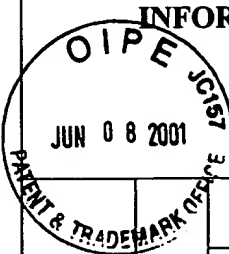
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	Applicant: Rajski et al.	
	Filed: July 20, 2000	Art Unit: 2858

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pmc			J. Rajski, J. Tyszer, N. Zacharia, "Test Data Decompression for Multiple Scan Designs with Boundary Scan," IEEE Transactions on Computers, Vol. 47, No. 11, November 1998
pmc			J. Rajski and J. Tyszer, "Design of Phase Shifters for BIST Applications", *Proc. VLSI Test Symposium*, pp. 218-224, 1998.
pmc			J. Rajski, N. Tamarapalli, J. Tyszer, "Automated Synthesis of Large Phase Shifters for Built-In Self-Test," International Test Conference, Paper 41.1, pp. 1047-1056, IEEE 1998
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		~~09/713,664~~	~~11/15/2000~~	~~Rajski et al.~~			
		~~09/619,988~~	~~07/20/2000~~	~~Rajski et al.~~			
		~~09/620,023~~	~~07/20/2000~~	~~Rajski et al.~~			
		~~09/619,985~~	~~07/20/2000~~	~~Rajski et al.~~			

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Note: These US patent applications had not been considered by Examiner because this US patent applications cann't be (considered) listed as Prior Art for a suggestion these copending US patent applications should be listed in the specification.

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Applicant: Rajski et al.

Filed: July 20, 2000   Art Unit: 2858 2153

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ꝑme		5,991,898	11/23/1999	Rajski et al.			
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ꝑme			I. Hamzaoglu, J. Patel, "Reducing Test Application Time for Full Scan Embedded Cores," Center for Reliable & High-Performance Computing, University of Illinois, Urbana, IL., 1999 IEEE
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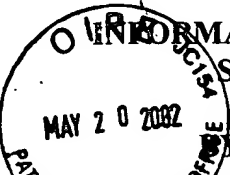| EXAMINER: Dhung Chung	DATE 9/26/02

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ꝑmc			J. Rajski, J. Tyszer, N. Zacharia, "Decompression of Test Data Using Variable-Length Seed LFSRs" Microelectronics and Computer Systems Laboratory, McGill University, Montreal, Canada, 1995 IEEE

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	Applicant: Rajski et al.	
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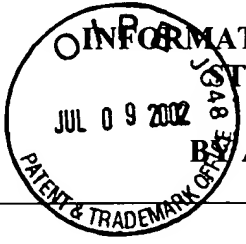| Init.*		Number	Date	Name	Class	Sub	Filed
Pmc		4,513,418	4/1985	Bardell, Jr. et al.			
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		5,592,493	1/1997	Crouch et al.			
		6,158,032	12/2000	Currier et al.			
Pmc		6,300,885	10/2001	Davenport et al.			

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Docket: 1011-54375	App: 09/620,021
	Applicant: Rajski et al.	
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Pmc		6,385,750	May, 2002	Kapur et al.			

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